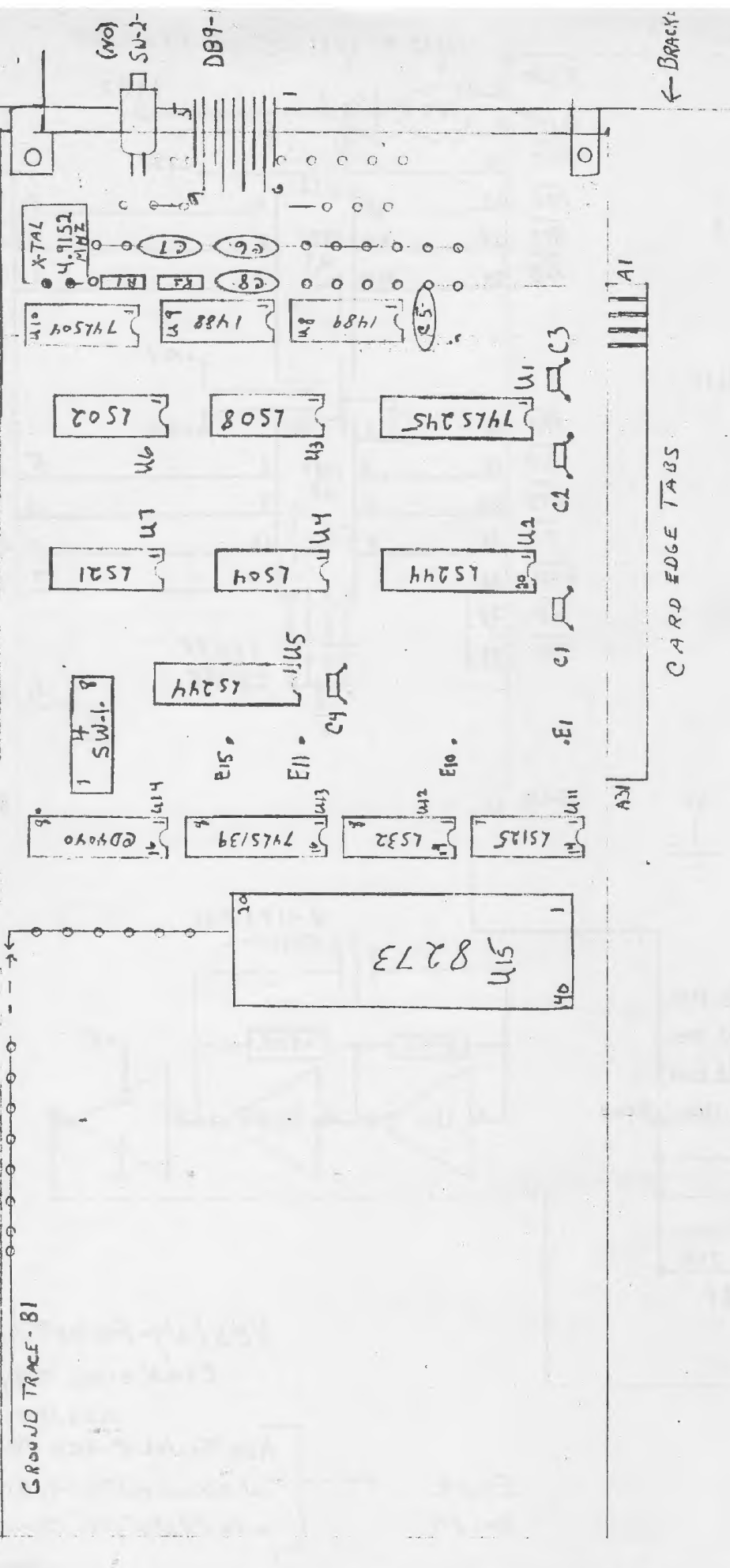
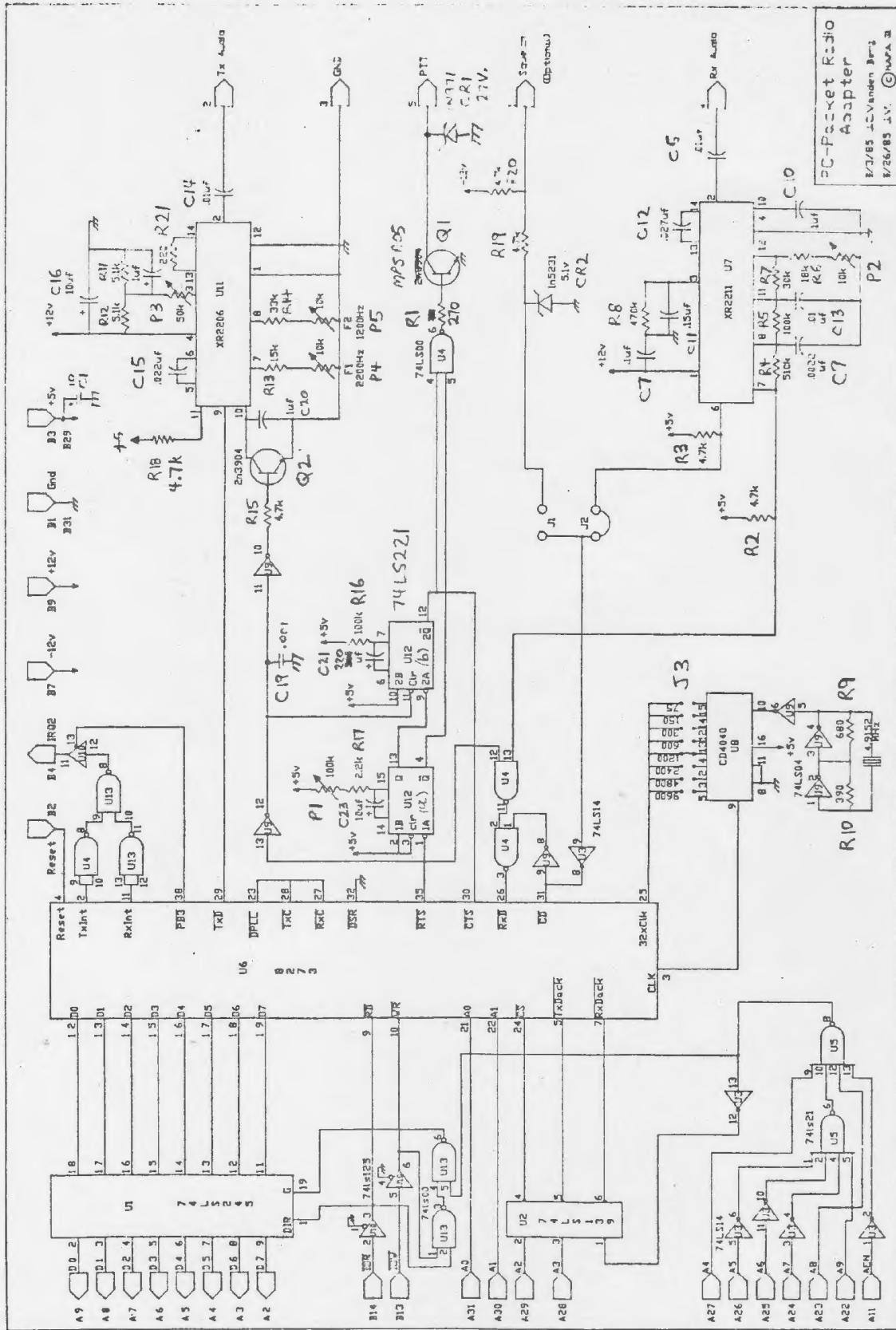


BOARD CONTINUES.  
 BLANK SPACE

IBM PROTOTYPE CARD WITH JACK BOTNER'S TNC ADAPTER (VE3LWY)  
 CLOCK CIRCUIT MODIFIED BY JOHN VANDENBERG (VE3DUU)



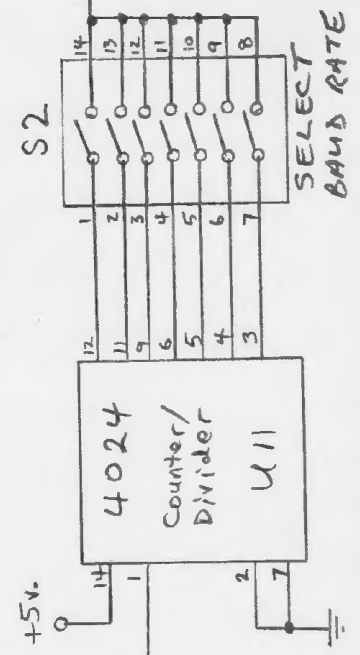
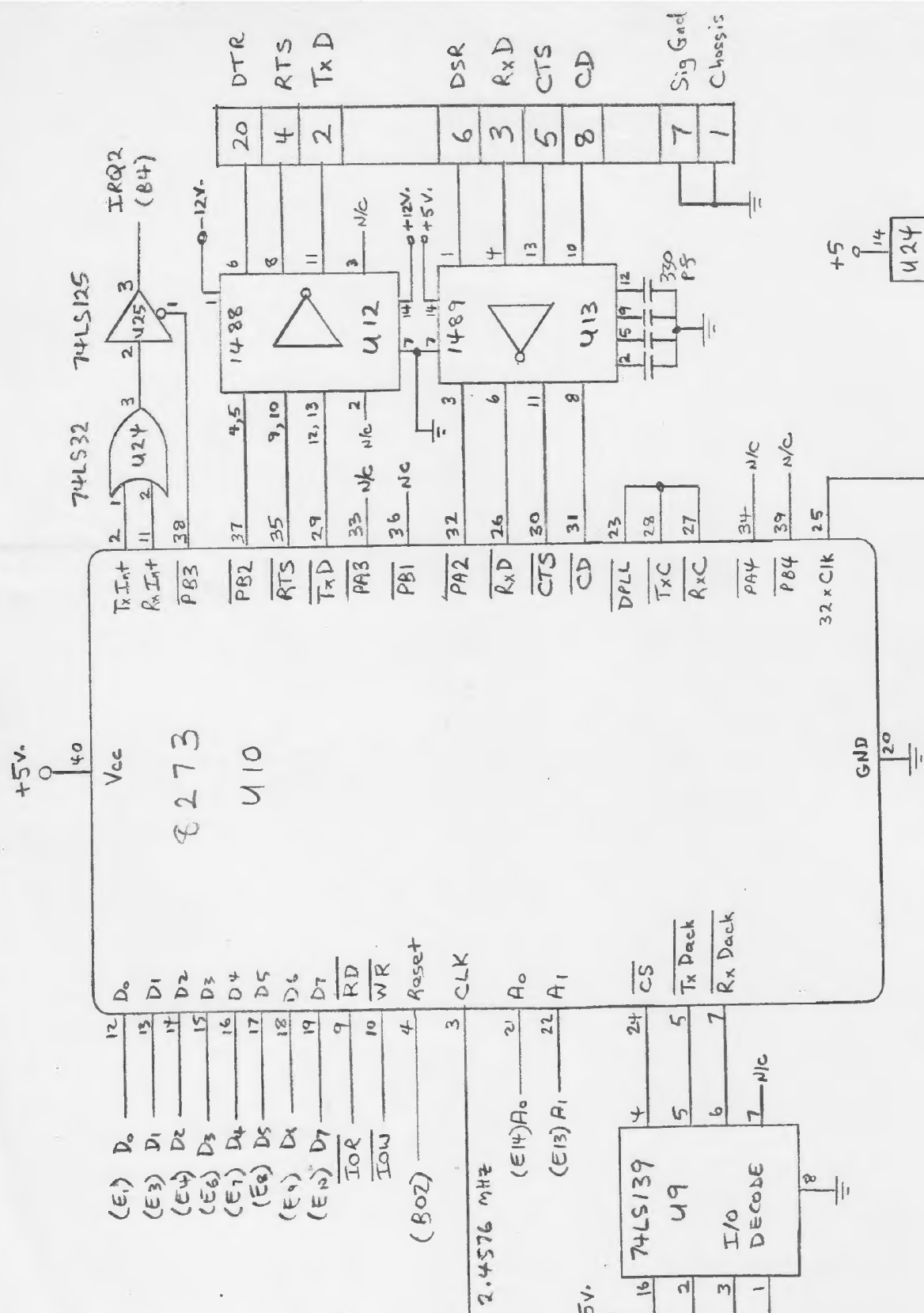
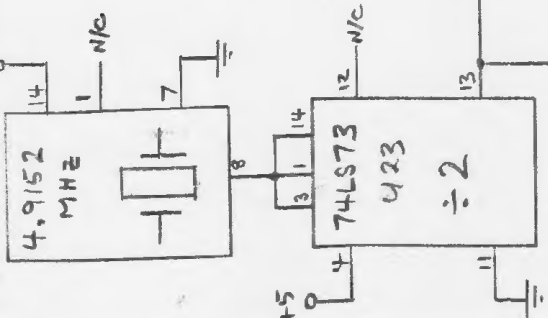
NOTE: U4 PIN 7 TO GROUND  
 TRACE MISSING ON CARD



C2, C3, C4, C6, C8, C17, C18, C22 (Not shown) bypass for supply of I.C.s (1.1uF)

DC-Pocket Radio Adapter  
E/7/85 J.C. Vanden Bergh  
E/26/85 J.V. © 1985 J.V.

HYBRID  
OSCILLATOR +5V.



8273 Adapter for IBM-PC  
by VE3LNY 09/83  
uses I/O decoding on  
prototype board.